ConnectX®-6 EN Card
200GbE Ethernet Adapter Card

World's first 200GbE Ethernet network adapter card, offering industry-leading performance, smart offloads and In-Network Computing, leading to the highest return on investment for Cloud, Web 2.0, Big Data, Storage and Machine Learning applications.

ConnectX-6 EN provides two ports of 200GbE for Ethernet connectivity, sub 0.8usec latency and 215 million messages per second, enabling the highest performance and most flexible solution for the most demanding data center applications.

ConnectX-6 is a groundbreaking addition to the Mellanox ConnectX series of industry-leading adapter cards. In addition to all the existing innovative features of past versions, ConnectX-6 offers a number of enhancements to further improve performance and scalability. ConnectX-6 EN supports 200, 100, 50, 40, 25, and 10GbE Ethernet speeds. ConnectX-6 adapter cards offer the next gen SFP-DD modules for the lower speed rates (50GGbE/100GbE), enabling double the SFP bandwidth and supporting fast line rates while maintaining SFP backwards compatibility.

Cloud and Web2.0 Environments

Telco, Cloud and Web2.0 customers developing their platforms on Software Defined Network (SDN) environments are leveraging the Virtual Switching capabilities of the Operating Systems on their servers to enable maximum flexibility in the management and routing protocols of their networks.

Open V-Switch (OVS) is an example of a virtual switch that allows Virtual Machines to communicate among themselves and with the outside world. Software-based virtual switches, traditionally residing in the hypervisor, are CPU intensive, affecting system performance and preventing full utilization of available CPU for compute functions.

To address this, ConnectX-6 offers ASAP® - Mellanox Accelerated Switch and Packet Processing® technology to offload the vSwitch/vRouter by handling the data plane in the NIC hardware while maintaining the control plane unmodified. As a result, significantly higher vSwitch/vRouter performance is achieved without the associated CPU load.

The vSwitch/vRouter offload functions supported by ConnectX-5 and ConnectX-6 include encapsulation and de-capsulation of overlay network headers, as well as stateless offloads of inner packets, packet headers re-write (enabling NAT functionality), hairpin, and more.

In addition, ConnectX-6 offers intelligent flexible pipeline capabilities, including programmable flexible parser and flexible match-action tables, which enable hardware offloads for future protocols.

HIGHLIGHTS

FEATURES
- Up to 200GbE connectivity per port
- Maximum bandwidth of 200Gb/s
- Up to 215 million messages/sec
- Sub 0.8usec latency
- Block-level XTS-AES mode hardware encryption
- Optional FIPS-compliant adapter card
- Support both 50G SerDes (PAM4)-and 25 SerDes (NRZ)-based ports
- Best-in-class packing with sub-nanosecond accuracy
- PCIe Gen4 and PCIe Gen3 support
- RoHS compliant
- ODCC compatible

BENEFITS
- Most intelligent, highest performance fabric for compute and storage infrastructures
- Cutting-edge performance in virtualized HPC networks including Network Function Virtualization (NFV)
- Advanced storage capabilities including block-level encryption and checksum offloads
- Host Chaining technology for economical rack design
- Smart interconnect for x86, Power, Arm, GPU and FPGA-based platforms
- Flexible programmable pipeline for new network flows
- Enabler for efficient service chaining
- Efficient I/O consolidation, lowering data center costs and complexity

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† For illustration only. Actual products may vary.
Storage Environments

NVMe storage devices are gaining momentum, offering very fast access to storage media. The evolving NVMe over Fabric (NVMe-oF) protocol leverages RDMA connectivity to remotely access NVMe storage devices efficiently, while keeping the end-to-end NVMe model at lowest latency. With its NVMe-oF target and initiator offloads, ConnectX-6 brings further optimization to NVMe-oF, enhancing CPU utilization and scalability.

Security

ConnectX-6 block-level encryption offers a critical innovation to network security. As data in transit is stored or retrieved, it undergoes encryption and decryption. The ConnectX-6 hardware offloads the IEEE AES-XTS encryption/decryption from the CPU, saving latency and CPU utilization. It also guarantees protection for users sharing the same resources through the use of dedicated encryption keys.

By performing block-storage encryption in the adapter, ConnectX-6 excludes the need for self-encrypted disks. This gives customers the freedom to choose their preferred storage device, including byte-addressable and NVDIMM devices that traditionally do not provide encryption. Moreover, ConnectX-6 can support Federal Information Processing Standards (FIPS) compliance.

ConnectX-6 also includes a hardware Root-of-Trust (RoT), which uses HMAC relying on a device-unique key. This provides both a secure boot as well as cloning-protection. Delivering best-in-class device and firmware protection, ConnectX-6 also provides secured debugging capabilities, without the need for physical access.

Machine Learning and Big Data Environments

Data analytics has become an essential function within many enterprise data centers, clouds and Hyperscale platforms. Machine learning relies on especially high throughput and low latency to train deep neural networks and to improve recognition and classification accuracy. As the first adapter card to deliver 200GbE throughput, ConnectX-6 is the perfect solution to provide machine learning applications with the levels of performance and scalability that they require.

ConnectX-6 utilizes the RDMA technology to deliver low-latency and high performance. ConnectX-6 enhances RDMA network capabilities even further by delivering end-to-end packet level flow control.

Socket Direct

Mellanox’s Socket Direct technology improves the performance of dual-socket servers in numerous ways, such as by enabling each of their CPUs to access the network through a dedicated PCIe interface. As the connection from each CPU to the network bypasses the QPI (UPI) and the second CPU, Socket Direct reduces latency and CPU utilization. Moreover, each CPU handles only its own traffic (and not that of the second CPU), thus optimizing CPU utilization even further.

Socket Direct also enables GPUDirect® RDMA for all CPU/GPU pairs by ensuring that GPUs are linked to the CPUs closest to the adapter card. Socket Direct enables Intel® DDIO optimization on both sockets by creating a direct connection between the sockets and the adapter card.

Socket Direct technology is enabled by a main card housing the ConnectX-6 and an auxiliary PCIe card bringing in the remaining PCIe lanes. The ConnectX-6 Socket Direct card is installed into two PCIe x16 slots and connected using a 350mm long harness. The two PCIe x16 slots may also be connected to the same CPU. In this case the main advantage of the technology lies in delivering 200GbE to servers with PCIe Gen3-only support.

Host Management

Mellanox host management and control capabilities include NC-SI over MCTP over SMBus, and MCTP over PCIe - Baseboard Management Controller (BMC) interface, as well as PLDM for Monitor and Control DSP0248 and PLDM for Firmware Update DSP0267.

Compatibility

<table>
<thead>
<tr>
<th>PCI Express Interface</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCIe Gen 4.0, 3.0, 2.0, 1.1 compatible</td>
</tr>
<tr>
<td>2.5, 5.0, 8, 16GT/s link rate</td>
</tr>
<tr>
<td>32 lanes as 2x16-lanes of PCIe</td>
</tr>
<tr>
<td>Support for PCIe x1, x2, x4, x8, and x16 configurations</td>
</tr>
<tr>
<td>PCIe Atomic</td>
</tr>
<tr>
<td>TLP (Transaction Layer Packet) Processing Hints (TPH)</td>
</tr>
<tr>
<td>PCIe switch Downstream Port Containment (DPC) enablement for PCIe hot-plug</td>
</tr>
<tr>
<td>Advanced Error Reporting (AER)</td>
</tr>
<tr>
<td>Access Control Service (ACS) for peer-to-peer secure communication</td>
</tr>
<tr>
<td>Process Address Space ID (PASID) Address Translation Services (ATS)</td>
</tr>
<tr>
<td>IBM CAPIv2 (Coherent Accelerator Processor Interface)</td>
</tr>
<tr>
<td>Support for MSI/MSI-X mechanisms</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Operating Systems/ Distributions*</th>
</tr>
</thead>
<tbody>
<tr>
<td>RHEL, SLES, Ubuntu and other major Linux distributions</td>
</tr>
<tr>
<td>Windows</td>
</tr>
<tr>
<td>FreeBSD</td>
</tr>
<tr>
<td>VMware</td>
</tr>
<tr>
<td>OpenFabrics Enterprise Distribution (OFED)</td>
</tr>
<tr>
<td>OpenFabrics Windows Distribution (WinOF-2)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Connectivity</th>
</tr>
</thead>
<tbody>
<tr>
<td>Up to two network ports</td>
</tr>
<tr>
<td>Interoperability with Ethernet switches (up to 200GbE, as 4 lanes of 50GbE data rate)</td>
</tr>
<tr>
<td>Passive copper cable with ESD protection</td>
</tr>
<tr>
<td>Powered connectors for optical and active cable support</td>
</tr>
</tbody>
</table>

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**FEATURES***

**Ethernet**
- 200Gbe / 100Gbe / 50Gbe / 40Gbe / 25Gbe / 10Gbe / 1Gbe
- IEEE 802.3bj, 802.3bm 100 Gigabit Ethernet
- IEEE 802.3by, Ethernet Consortium 25, 50 Gigabit Ethernet, supporting all FEC modes
- IEEE 802.3ba 40 Gigabit Ethernet
- IEEE 802.3ae 10 Gigabit Ethernet
- IEEE 802.3az Energy Efficient Ethernet
- IEEE 802.3ap based auto-negotiation and KR startup
- IEEE 802.3ad, 802.1AX Link Aggregation
- IEEE 802.1Q, 802.1P VLAN tags and priority
- IEEE 802.1Qbg (ICN) – Congestion Notification
- IEEE 802.1Qaz (ETS)
- IEEE 802.1Qau (QCN) – Congestion
- IEEE 802.3ad, 802.1AX Link Aggregation
- IEEE 802.1Qbg
- IEEE 1588v2

**Enhanced Features**
- Hardware-based reliable transport
- Collective operations offloads
- Vector collective operations offloads
- PeerDirect™ RDMA (aka GPU Direct®) communication acceleration
- 64/66 encoding
- Enhanced Atomic operations
- Advanced memory mapping support, allowing user mode registration and remapping of memory (UMR)
- Extended Reliable Connected transport (XRC)
- Dynamically Connected transport (DCT)
- On-demand paging (OPD)
- MPI Tag Matching
- Rendezvous protocol offload
- Out-of-order RDMA supporting Adaptive Routing
- Burst buffer offload
- In-Network Memory registration-free RDMA memory access

**CPU Offloads**
- RDMA over Converged Ethernet (RoCE)
- TCP/UDP/IP stateless offload
- LSO, LRO, checksum offload
- RSS (also on encapsulated packet), TSS, HDS, VLAN and MPLS tag insertion/stripping, Receive flow steering
- Data Plane Development Kit (DPDK) for kernel bypass application
- Open VSwitch (OVS) offload using ASAP2
- Flexible match-action flow tables Tunneling encapsulation / de-encapsulation

**Overlay Networks**
- RoCE over overlay networks
- Stateless offloads for overlay network tunneling protocols
- Hardware offload of encapsulation and decapsulation of VXLAN, NVGRE, and GENEVE overlay networks

**HPC Software Libraries**
- HPC-X, OpenMPI, MVAPICH, MPICH, OpenSVMEM, PGAS and varied commercial packages

**Management and Control**
- NC-SI, MCTP over SMBus and MCTP over PCIe - Baseboard Management Controller interface
- PLDM for Monitor and Control
- SDN management interface for managing the eSwitch
- PC interface for device control and configuration
- General Purpose I/O pins
- SPI interface to Flash
- JTAG IEEE 1149.1 and IEEE 1149.6

**Remote Boot**
- Remote boot over Ethernet
- Remote boot over iSCSI
- Unified Extensible Firmware Interface (UEFI)
- Pre-execution Environment (PXE)

(*) This section describes hardware features and capabilities. Please refer to the driver and firmware release notes for feature availability.

**Table 1 - Part Numbers and Descriptions**

<table>
<thead>
<tr>
<th>OPN</th>
<th>Ethernet Supported Speeds (GbE)</th>
<th>No. of Network Ports</th>
<th>Cage(s)</th>
<th>PCI Express Configuration</th>
<th>Form Factor</th>
</tr>
</thead>
<tbody>
<tr>
<td>MCX614106A-VCAT</td>
<td>200, 100, 50, 40, 25, 10</td>
<td>2</td>
<td>QSF56</td>
<td>Socket Direct 2x PCIe3.0 x16</td>
<td>PCIE Standup</td>
</tr>
<tr>
<td>MCX614105A-VCAT</td>
<td>200, 100, 50, 40, 25, 10</td>
<td>1</td>
<td>QSF56</td>
<td>Socket Direct 2x PCIe3.0 x16</td>
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<td>MCX613105A-VDAT</td>
<td>200, 100, 50, 40, 25, 10</td>
<td>1</td>
<td>QSF56</td>
<td>PCIe 4.0 x16</td>
<td>PCIE Standup</td>
</tr>
<tr>
<td>MCX613106A-CCAT</td>
<td>100, 50, 40, 25, 10</td>
<td>2</td>
<td>QSF56</td>
<td>PCIe 3.0/4.0 x16</td>
<td>PCIE Standup</td>
</tr>
<tr>
<td>MCX613106A-VDAT</td>
<td>200, 100, 50, 40, 25, 10</td>
<td>2</td>
<td>QSF56</td>
<td>PCIe 4.0 x16</td>
<td>PCIE Standup</td>
</tr>
<tr>
<td>MCX613140A-VDAI</td>
<td>200, 100, 50, 40, 25, 10</td>
<td>2</td>
<td>QSF56</td>
<td>PCIe 4.0 x16</td>
<td>OCP 3.0 SFF</td>
</tr>
<tr>
<td>MCX613145A-VDAI</td>
<td>200, 100, 50, 40, 25, 10</td>
<td>1</td>
<td>QSF56</td>
<td>PCIe 4.0 x16</td>
<td>OCP 3.0 SFF</td>
</tr>
<tr>
<td>MCX613108A-CCAT*</td>
<td>50, 40, 25, 10</td>
<td>2</td>
<td>SFP-DD</td>
<td>PCIe 3.0/4.0 x16</td>
<td>PCIE Standup</td>
</tr>
</tbody>
</table>

(*) Please contact Mellanox for availability.