Mellanox’s SwitchX Development Kit (DVK), based on the fifth-generation switch silicon, accelerates time-to-market for switch and server OEMs designing high performance, cost-effective switching and bridging solutions for InfiniBand, Ethernet and Fibre Channel.

SwitchX silicon implements Virtual Protocol Interconnect (VPI) and the SwitchX DVK provides the flexibility to validate PHY, schematic and layout characteristics for all protocols supported. In addition, the SwitchX DVK offers a configurable platform for protocol stack and management software development. Paired with the Mellanox SwitchX Software Development Kit (SDK), the SwitchX DVK provides the materials needed to quickly introduce an InfiniBand or Ethernet switch system to the market.

The flexible combination of QSFP and SFP+ physical connectors, along with SwitchX’s fully integrated PHY allows system designers the ability to develop and validate software for Layer 2 bridging across various protocols (SDR/DDR/QDR/FDR10/FDR InfiniBand to 1/10/40/56GbE and 2/4/8G FC as well as 1/10/40/56GbE to 2/4/8G FC) well before custom hardware is ready for testing.

HIGHLIGHTS

BENEFITS
- Integrated with SwitchX Software Development Kit (SDK) for fast time-to-market
- Full support for Virtual Protocol Interconnect (VPI)
- Flexible port configuration
- Integrated PPC CPU
- Management PCIe bus extender to connect to additional CPU architectures

KEY FEATURES
- 4Tb/s Unified Switching/Routing
- SFP+, QSFP and SMA connectors independent of Layer 2 protocol

Figure 1. Mellanox Development Kit (DVK) Port Configuration
SwitchX Silicon
The SwitchX silicon supports many advanced InfiniBand and Ethernet features such as DCB, virtualization, NPIV Fibre Channel and Ethernet gateways as well as flexible port and PHY configurations. SwitchX SDK running on SwitchX DVK provides developers the capability to validate these features while porting/developing protocol stack and management SW.

Applications
SwitchX DVK is designed to accelerate development of managed single and multichip implementations for blade switches, top-of-rack (TOR) switches and large port count modular chassis aggregation switches providing Web 2.0 and Cloud scale-out, FCoIB and FCoE fabric convergence as well as Virtual I/O switching.

Physical Layer Validation
SwitchX DVK implements industry standard compliance channels for 10/40/56GbE. This allows developers to connect directly to an oscilloscope and measure signal specification adherence.

Utilizing software tools provided with the DVK, developers can also tune the PHY transmit and receive parameters, generate PRBS and calculate BER.

Port Configuration
SwitchX DVK allows provisioning of the VPI ports for SDR/DDR/ODR/FDR10/FDR InfiniBand, 1/10/20/40/56Gb/s and Ethernet. The ease of provisioning via VPI provides flexibility to validate multi-protocol switching as well as routing and bridging technologies prior to custom hardware being available.

Figure 2. Mellanox SwitchX Architecture

Ordering Information

<table>
<thead>
<tr>
<th>Ordering Part Number</th>
<th>InfiniBand 4X Port Speed</th>
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<tbody>
<tr>
<td>MT51336-EN-EVB</td>
<td>10/40/56 Gigabit Ethernet Development Kit</td>
</tr>
<tr>
<td>MT51336-VPI-EVB</td>
<td>FDR IB and 10/40/56 Gigabit Ethernet VPI Development Kit</td>
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